



**FACULTY DEVELOPMENT PROGRAMME on  
Machine Learning for Signal Processing  
(25<sup>th</sup> October -05<sup>th</sup> November, 2021)  
Organized by**



**Department of Electronics & Communication Engineering  
LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY**

*An UGC Autonomous Institution, NAAC "A" Grade, NBA Accredited*  
**Vizag-Vizianagaram Road-NH-43,Denkada, Jonnada, Andhra Pradesh 535005**

**in association with  
E & ICT Academy, NIT Warangal  
(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)**

**Preamble:**

Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. The role of this academy role is to offer Faculty Development Programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

This Faculty Development Programme (FDP) is devoted to address the need to enhance the knowledge about the latest technologies pertaining to "MACHINE LEARNING FOR SIGNAL PROCESSING" is being conducted at Lendi Institute of Engineering and Technology, Vizianagaram, the course is handled by academicians and industry experts.

**Major Course Contents:**

Signal Processing	Fundamentals of Signal Processing,
	Signal Transformation
	Spectral Analysis.
ML	Supervised and Unsupervised Learning
	Classification and Regression(Linear Models)
	Stochastic Process
	Feature Extraction
	Fuzzy Logic and Neural Networks
	Markov Model,
	Implementation in audio and speech recognition
Applications	Biomedical Signal Processing,
	Wireless Communication,
	Imaging applications etc.
	Introduction to Python Programming.
	Hands on Session on Python Programming

**conducting faculty & Resource persons in this programme:**

The programme will be conducted by the faculty members from NIT Warangal and Lendi Institute of Engineering and Technology (LIET). Academicians in the concerned field from IITs/NITs/IIITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the programme.

**Eligibility:**

The programme is open to the Faculty of Engineering Colleges, MCA Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

**Selection Criteria:**

Selection will be done based on **first-come-first-serve** basis to a maximum number of 100. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back.

Candidates will be issued satisfactory certificates on successful completion of the course.

Reservations are followed for selecting candidates as per GOI norms.

**About NIT Warangal:**

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute, imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D. programs in various specializations. All B.Tech and M.Tech programmes of NIT Warangal are NBA accredited.

**About LENDI Vizianagaram:**

Lendi is a premiere Institution is established in 2008 by a divine body of committed intelligentsia under the aegis of Sai Dhamam Educational Trust of Visakhapatnam to cater to the needs of young graduates of technology. It is a lush green campus with eco-friendly environment. Ours is an Autonomous College which is spread to all directions with a strength of over 2600 students and above 200 faculty. College is accredited by NAAC with "A" Grade, NBA with six branches and permanently affiliated by JNTU Kakinada.

**About the Department:**

Department of ECE was started with an initial intake of 60 students (in 2008) and gradually enhanced to 180 students intake at graduation level. Post-Graduation M.Tech program in Embedded Systems & VLSI Design was introduced in 2014 – 2015 with an intake of 18 seats. The department is strengthened by its eminent faculty of 7 doctorates, 13 pursuing doctorates and senior faculty. Department is equipped with state of art infrastructure comprising ICT class rooms, advanced laboratories including Industry sponsored lab, dedicated projects lab and R&D lab supported by licensed software tools like Mentor Graphics, MATLAB, XLINX Vivado and ANSYS-HFSS etc

**Registration Fee and Payment details:**

Faculty and Research Scholars	<b>Rs. 750 /-</b>
Industry Participants	<b>Rs. 2250 /-</b>
<b>Online payment Details:</b>	
<b>Account Name: Electronics &amp; ICT Academy</b>	
<b>NITWAccount No: 62423775910</b>	
<b>IFSC: SBIN0020149</b>	
<b>Bank and Branch: State Bank of India, NIT(REC) Warangal</b>	

**How to apply:**

Participants are required to fill the online registration form through the Following Link:

<https://forms.gle/GkNrrNzPgF4xfQtD6>

**Calendar dates to Note:**

Last date to submit Registration	<b>23-10-2021</b>
Selection of Applicant by E-mail	<b>24-10-2021</b>
FDP duration	<b>25-10-2021 to 05-11-2021</b>



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1. Name :
2. Designation :
3. Institution :
4. Email :
5. Reference No :  
Bank :  
Date :  
Amount :
6. Address for Correspondence :
7. Educational Qualification :
8. Subjects taught so far :
9. No. of refresher courses/workshops attended:
10. Experience (in years):  
Teaching:      Research:      Industry:
11. Do you belong to SC/ST: YES / NO

**Address for correspondence**

*Post your application form (if it becomes offline)*

**Dr. A.V.Paramkusam**

**Professor & Vice Principal  
(Academics)**

**Electronics & Communication Engineering  
Lendi Institute of Engineering & Technology  
Jonnada (V), Denkada (M)  
Vizianagaram – 535005, Andhra Pradesh, India.**

*For more details about Electronics & ICT Academy, NIT, Warangal,  
please visit: <https://nitw.ac.in/eict>*

**Declaration:**

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

*For more enquiries please contact:*

**Dr. B.Kiranmai**

**Professor, Dept. of ECE  
Mobile: 8500267193**

**Mr. S.S.Kiran**

**Assistant Professor, Dept. of ECE  
Mobile: 9985163700**

**Mr.K.Gurucharan**

**Assistant Professor, Dept. of ECE  
Mobile: 7702286314**

**Mr.L.Srinivas Rao**

**Assistant Professor, Dept. of ECE  
Mobile: 6304026294**

**SPONSORSHIP CERTIFICATE:**

Dr. /Mr. /Ms. .... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "Machine Learning for Signal Processing", sponsored by Electronics & ICT Academy during 25<sup>th</sup> OCT-5<sup>th</sup> NOV,2021 at Lendi Institute of Engineering and Technology, Vizianagaram.

Signature of Head of Institution

(With seal)