**LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY**
An Autonomous Institution
Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada
Accredited by NAAC with "A" Grade)
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## AUTONOMOUS EXAMINATION CELL

LIET /Exam.Cell(A)/2020-21                                                        Date:24-04-2021

**I M.TECH - I SEMESTER (R19REGULATIONS) I MID EXAMINATIONS – APRIL, 2021**
**TIME TABLE**

| Date & Day | Computer Science and Engineering | Embedded Systems & VLSI Design | Power System and Control Automation | Machine Design |
|---|---|---|---|---|
| 03-05-2021 Monday 2.00 PM to 3.30 PM | Advanced data structures & Algorithms Analysis | VLSI Technology | Microprocessors & Microcontrollers | Computational Methods in Engineering |
| 04-05-2021 Tuesday 2.00 PM to 3.30 PM | Cryptography & Network Security | Embedded System Design | HVDC Transmission | Advanced Mechanics of Solids |
| 05-05-2021 Wednesday 2.00 PM to 3.30 PM | Computer Organization & Architecture | Digital Design Through Verilog | Power System Operation and Control | Advanced Mechanisms |
| 06-05-2021 Thursday 2.00 PM to 3.30 PM | Software Architecture & Design Patterns | Programming Embedded System | Reactive Power Compensation & Management | Mechanical Vibrations |
| 07-05-2021 Friday 2.00 PM to 3.30 PM | **Elective-I** 1.Mobile Computing 2.Advanced Computer Networks 3.Internet of Things 4.Social Networks & Semantic Web | **Elective-I** 1. VLSI Digital Signal Processing systems 2. System-on-Chip design 3. Design for Testability | **Elective – I** 1. Electrical Distribution Systems 2. Analysis of Power Electronics Converters 3. Renewable Energy Systems 4. Artificial Intelligence Techniques | **Elective – I** 1. Design of Automobile Systems 2. Product Design 3. Geometric Modeling 4. Non Destructive Evaluation |

| Date & Day | Computer Science and Engineering | Embedded Systems & VLSI Design | Power System and Control Automation | Machine Design |
|---|---|---|---|---|
| 08-05-2021 Saturday 2.00 PM to 3.30 PM | **Elective-II** 1. Image Processing 2. Pattern Recognition 3. Optimization Techniques 4. Computer Vision | **Elective -II** 1. System design with Embedded Linux 2. Micro Electro Mechanical Systems (MEMS) Design 3. Digital signal processor and architecture | **Elective – II** 1. Power System Security 2. Advanced Digital Signal Processing 3. Programmable Logic Controllers &Applications 4. Modern Control Theory | **Elective – II** 1. Fracture Mechanics 2. Gear Engineering 3. Design for Manufacturing &Assembly 4. Continuum Mechanics |
| 10-05-2021 Monday 2.00 PM to 3.30 PM | Audit Course 1:English For Research Paper Writing | Audit Course 1:English For Research Paper Writing | Audit Course 1:English For Research Paper Writing | Audit Course 1:English For Research Paper Writing |

**Prof K.V. Narasimham**
**Controller of Examination**

**Dr.V.V.Rama Reddy**
**Principal**