

LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY

An Autonomous Institution Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada Accredited by NAAC with "A" Grade) Jonnada (Village), Denkada (Mandal), Vizianagaram Dist – 535 005 Phone No. 08922-241111, 241112

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AUTONOMOUS EXAMINATION CELL

LIET /Exam.Cell(A)/2019-20

Date:11-01-2020

I M.TECH - I SEMESTER (R19REGULATIONS) SEMESTER END EXAMINATIONS – FEBRUARY, 2020

TIME TABLE

Date & Day	Computer Science and Engineering	Embedded Systems & VLSI Design	Power System and Control Automation	Machine Design
03-02-2020 Monday 1.00 PM to 4.00 PM	Advanced data structures & Algorithms Analysis	VLSI Technology	Microprocessors & Microcontrollers	Computational Methods in Engineering
04-02-2020 Tuesday 1.00 PM to 4.00 PM	Cryptography & Network Security	Embedded System Design	HVDC Transmission	Advanced Mechanics of Solids
05-02-2020 Wednesday 1.00 PM to 4.00 PM	Computer Organization & Architecture	Digital Design Through Verilog	Power System Operation and Control	Advanced Mechanisms
06-02-2020 Thursday 1.00 PM to 4.00 PM	Software Architecture & Design Patterns	Programming Embedded System	Reactive Power Compensation & Management	Mechanical Vibrations
07-02-2020 Friday 1.00 PM to 4.00 PM	Elective-I 1.Mobile Computing 2.Advanced Computer Networks 3.Internet of Things 4.Social Networks & Semantic Web	Elective-I 1. VLSI Digital Signal Processing systems 2. System-on-Chip design 3. Design for Testability	 Elective – I 1. Electrical Distribution Systems 2. Analysis of Power Electronics Converters 3. Renewable Energy Systems 4. Artificial Intelligence Techniques 	 Elective – I Design of Automobile Systems Product Design Geometric Modeling Non Destructive Evaluation

Date & Day	Computer Science and Engineering	Embedded Systems & VLSI Design	Power System and Control Automation	Machine Design
	Elective-II	Elective -II	Elective – II	Elective – II
	1. Image Processing	1. System design with	1. Power System Security	1. Fracture Mechanics
08-02-2020	2. Pattern Recognition	Embedded Linux	2. Advanced Digital Signal	2. Gear Engineering
Saturday	3. Optimization	2. Micro Electro Mechanical	Processing	3. Design for Manufacturing
1.00 PM to 4.00 PM	Techniques	Systems (MEMS) Design	3. Programmable Logic	&Assembly
	4. Computer Vision	3. Digital signal processor and	Controllers & Applications	4. Continuum Mechanics
		architecture	4. Modern Control Theory	

K. U. Narahithan

Prof K.V.Narasimham Controller of Examination $V.V \sim J$

Dr.V.V.Rama Reddy Principal